

(19)



Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 0 717 564 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:

19.06.1996 Bulletin 1996/25

(51) Int. Cl.⁶: **H04N 5/913**

(21) Application number: **95308482.9**

(22) Date of filing: **27.11.1995**

(84) Designated Contracting States:
DE FR GB

(30) Priority: **26.11.1994 KR 9431373**

(71) Applicant: **LG ELECTRONICS INC.**
Seoul (KR)

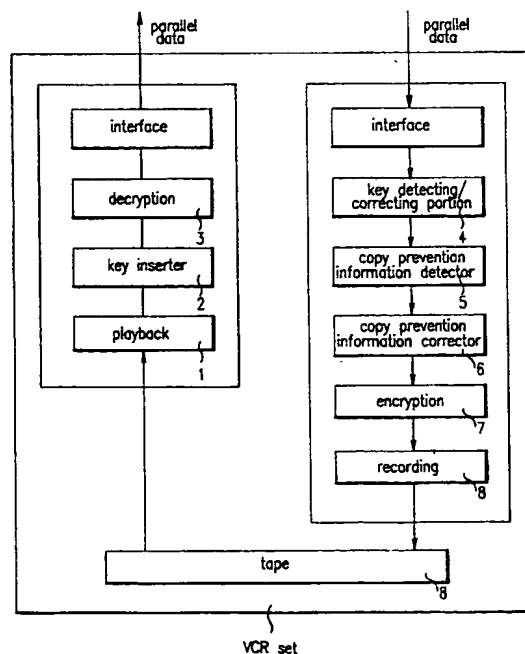
(72) Inventor: **Park, Tae Joon**
Seoul (KR)

(74) Representative: **Cross, Rupert Edward Blount et al**
BOULT, WADE & TENNANT
27 Furnival Street
London EC4A 1PQ (GB)

(54) Copy prevention method and apparatus for digital video system

(57) A copy prevention method and apparatus for a digital video system is disclosed including the steps of:
(a) adding a header area of a header start code and key field to a reproduced bit stream; (b) decrypting and transmitting the bit stream to which the header area is added;
(c) detecting a key field of the decrypted and transmitted bit stream and detecting copy prevention information; and (d) encrypting the bit stream according to information detected from step (c) and recording it on tape.

FIG.5



EP 0 717 564 A2

Description

Background of the Invention

The present invention relates to a copy prevention technology for a digital video system, and more particularly, to a copy prevention method and apparatus for a digital VCR to which encryption is introduced to display a picture only in a VCR internally containing a corresponding encryption code, thereby preventing tape from being copied.

General copy prevention methods for analog VCR are presented in U.S. Patent Nos. 4819098, 4571642 and 4577216.

First, U.S. patent No. 4819098 discloses a method in which an interference signal is inserted into a video waveform in an automatic gain control circuit (AGC) of a VCR. Here, the inserted signal does not affect the AGC of its monitor but has the AGC of the VCR record an accurate level of signal on a video tape.

In U.S. patent No. 4571642, there is presented a method in which a phase noise or other corrected signal is inserted into the chroma burst of a video waveform.

However, all the conventional technologies insert a distributing signal to an analog signal using the difference between a circuit of a monitor and a corresponding circuit of a VCR. Some VCRs may perform copy normally despite of copy prevention. Some monitors cannot display images of the original video tape. A conventional copy prevention introduced to an analog VCR system is hard to be applied to digital storage media (DSM).

Specifically, in a satellite receiver or high-definition TV decoder, as shown in Fig. 2, an MPEG bit stream received by a digital VCR is constructed to transmit a transport header, packetized elementary stream (PES) header and audio and video data respectively or simultaneously.

The PES header contains a PES header flag area of 14 bits which is a field for DSM such as digital VCR, and a PES header field having a variable length. The PES header flag area includes 1-bit copyright (CR) flag, 1-bit original-or-copy (OC) flag, 2-bit PD flag, 1-bit TM flag, and 1-bit AC flag.

The PES header field varies in length, and part thereof is set by the PD, TM and AC flags. A PTS/DTS area is not present if the value of the PD flag is "00". It is 40 bits if the value "10". If the value is "11", the area is 80 bits. A DSM trick mode field is not present if the TM flag is "0". If the flag is "1", the field is 8 bits. An additional copy information field is 8 bits if the AC flag is "1".

When recording is carried out by the satellite receiver or high-definition TV decoder and compressed video data is encoded in encoder 101, it is converted into a packet form in packet processing portion 102 as shown in Fig. 1. If the compressed audio data is encoded in audio encoder 103, it is converted into a packet form in packet processing portion 104.

When the outputs of packet processing portions 102 and 104 are multiplexed in transmission multiplexer 105,

a fixed transmission stream shown in Fig. 2 is output to a digital VCR. In this case, for copy prevention, a public-key encryption is applied which is suggested in U.S. Patent No. 4200770. This solves disadvantages in key management or key distribution when a conventional block-cipher or stream cipher algorithm such as data encryption standard (DES) encrypts or decrypts only with a secret key.

This public-key encryption system has all users U hold unique encryption algorithm E^{PK_U} and decryption algorithm D^{PK_U} . Here, encryption algorithm E^{PK_U} for the public-key is opened as a public-key to key supply portion 107. Decryption algorithm D^{PK_U} for secret key is kept in secret. The characteristics of E^{PK_U} and D^{PK_U} are as follows.

First, with respect to all users U and message m transmitted, $D^{PK_U}(E^{PK_U}(m)) = m$.

Second, encryption algorithm E^{PK_U} and decryption algorithm D^{PK_U} do not require complicated calculation.

Third, it is impossible to find D^{PK_U} satisfying $D^{PK_U}(E^{PK_U}(m)) = m$ from encryption algorithm E^{PK_U} .

In the encryption system having the above characteristics, as shown in Fig. 3, when user A transmits message m to user B, encrypter 106 receiving public-key algorithm E^{PK_B} for user B's public-key from key supply portion 107 encrypts message m ($E^{PK_B}(m) = c$) and transmits the result to decrypter 109 via a public channel. Here, the public channel indicates a channel in which transmitted data is not kept in secret.

Key decrypter 108 receiving the key information from key supply portion 107 outputs an algorithm D^{PK_B} corresponding to encryption algorithm E^{PK_B} . Decrypter 109 decrypts ($D^{PK_B}(c) = m$) the output of encrypter 106 with decryption algorithm D^{PK_B} , and then transmits user B. In other words, only user B can decrypt decryption algorithm D^{PK_B} corresponding to encryption algorithm E^{PK_B} .

A concept developed from the public-key encryption is presented in U.S. Patent No. 4405829. This public-key encryption system is called RSA system. A method in which the RSA public-key encryption is efficiently calculated via batch processing is presented in U.S. Patent No. 4964164.

However, this public-key encryption is inappropriate for high-velocity encryption. A CA system is to prevent illegal view. However, there is no method of protecting a program distributed through a digital storage medium such as digital VCR.

Summary of the Invention

Therefore, it is desirable to provide an illegal copy prevention method and apparatus for a digital video system in which, in copy tape, encrypted key information is transmitted and recorded so that a copied tape is reproducible only in a VCR having a corresponding encrypted key information, thereby preventing copy.

According to a first aspect of the present invention, there is provided a copy prevention method for a digital video system comprising the steps of: (a) adding a header area of a header start code and key field to a reproduced bit stream; (b) decrypting and transmitting the bit stream to which the header area is added; (c) detecting a key field of the decrypted and transmitted bit stream and detecting copy prevention information; and (d) encrypting the bit stream according to information detected from step (c) and recording it on tape.

According to a second aspect of the present invention, there is provided a copy prevention apparatus for a digital video system comprising: a reproduction block for adding key information to a reproduced bit stream, and decrypting and transmitting it; and a recording block for searching key information of the bit stream transmitted from the reproduction block to extract copy prevention information, and encrypting and recording the bit stream according to the extracted copy prevention information.

The reproduction block may comprise reproduction means for reproducing data recorded on tape; key insertion means for adding key information to the bit stream of the reproduction means; and decryption means for decrypting the output of the key insertion means and transmitting it to a recording-side VCR.

The recording block may comprise key detecting/correcting means for detecting key information from the transmitted bit stream of a reproducing-side VCR; copy prevention information detecting means for searching the key information detected from the key detecting/correcting means to detect copy prevention information; encrypting means for encrypting the bit stream according to the copy prevention information of the copy prevention information detecting means; and recording means for recording the bit stream encrypted in the encrypting means.

The copy prevention information detecting means may comprise a PES header detecting portion for detecting a PES header from parallel data output from the key detecting/correcting means; and a copy prevention information extractor enabled by a PES header detection signal of the PES header detecting portion to detect an additional copy information field.

Brief Description of the Attached Drawings

Fig. 1 is a block diagram of a conventional packet processing apparatus;

Fig. 2 shows an example of a general transmission stream;

Fig. 3 is a block diagram of a conventional public-key encryption system;

Fig. 4 shows connections of systems embodying the present invention;

Fig. 5 is a block diagram of a copy prevention apparatus for a digital video system embodying the present invention;

Fig. 6 is a block diagram of the copy prevention information detector of Fig. 5;

Fig. 7 is a circuit diagram of the PES header detector of Fig. 6;

Figs. 8A-8F are waveform diagrams of input/output at the respective portions of Fig. 7;

Fig. 9 is a circuit diagram of the copy prevention information extractor of Fig. 6;

Figs. 10A-10G are waveform diagrams of input/output at the respective portions of Fig. 9; and

Fig. 11 shows an example of a bit stream embodying the present invention.

Detailed Description of the Invention

Hereinafter, a preferred embodiment of the present invention will be described below with reference to the attached drawings.

Referring to Fig. 5, a copy prevention apparatus embodying the present invention comprises a reproducing portion 1 for reproducing data recorded on tape, a key inserting portion 2 for adding a tape header start code and key field at the front end of a bit stream of reproducing portion 1, a decrypting portion 3 for decrypting the output of key inserting portion 2 and transmitting it as parallel data, a key detecting/correcting portion 4 for detecting a key field from the parallel data transmitted from decrypting portion 3, a copy prevention information detecting portion 5 for detecting a PES header from the key field detected and extracting copy prevention information, a copy prevention information correcting portion 6 for correcting the output of copy prevention information detecting portion 5 if necessary, an encrypting portion 7 for encrypting the output of copy prevention information correcting portion 6, and a recording portion 8 for recording the output of encrypting portion 7 on tape.

As shown in Fig. 6, copy prevention information detecting portion 5 comprises a PES header detecting portion 10 for searching the parallel data in synchronization with a clock clk to detect the PES header, and a copy prevention information extractor 20 enabled by the PES header signal of PES header detecting portion 10 to detect the copy prevention information field.

Referring to Fig. 7, PES header detecting portion 10 comprises first and second flipflops 11 and 12 for sequentially delaying the parallel data according to clock clk, a packet start code detector 13 for searching the parallel data and the output of first and second flipflops 11 and 12 to detect the packet start code of the PES header, a stream ID detector 14 for searching the output of second flipflop 12 to detect the stream ID of the PES header, a delay 15 for sequentially delaying the output of packet start code detector 13 according to clock clk, and a detection signal generator 16 for logically multiplying the outputs of delay 15 and stream ID detector 14 and outputting a PES header detection signal is-PES-header.

As shown in Fig. 9, copy prevention information extractor 20 comprises a D-flipflop 21 for holding the parallel data output from PES header detector 10, a D-flipflop 22 for holding PES header detection signal is-PES-

header of PES header detector 10, a D-flipflop 23 cleared by the output of D-flipflop 22 and holding voltage (+5V) by a CR signal of the output of D-flipflop 21 and outputting a signal LCR, a D-flipflop 24 cleared by the output of D-flipflop 22 and holding voltage (+5V) by an OC signal of the output of D-flipflop 21 and outputting a signal LOR, a copy prevention information position operator 25 for searching the parallel data of PES header detector 10 and calculating the position of an additional copy information field, a counter 26 for counting the output of copy information position operator 25, and a D-flipflop 27 for holding the additional copy information field of the output of D-flipflop 21.

The operation and effect of the present invention will be explained below. Generally, in case of reproducing or copy recording data on tape, connections between systems are made as shown in Fig. 4.

With those connections, an MPEG bit stream reproduced from VCR A is input to a satellite receiver or high-definition TV so that it cannot be recognized whether the stream is displayed on a screen or input to VCR B and recorded on another video tape.

For this reason, according to the described embodiment, in case that the bit stream reproduced from VCR A is copied from VCR B, information on copy prevention is transmitted to VCR B from VCR A. VCR B analyzes this information which is recorded with the bit stream.

Here, the insertion position of the copy prevention information contained in a GA bit stream is very limited because it must not affect decoding of the decoder of the satellite receiver or high-definition TV so that an image is displayed normally on a monitor. The copy prevention information may be inserted into the front end of the MPEG bit stream or inside the PES header.

When the MPEG bit stream is decoded in units of group of picture (GOP), the respective GOPs are classified by their GOP start codes. This is useful in transmitting initialization data to a recording-side VCR because decoding is never affected even when a slight amount of data is added to the front end of the MPEG bit stream.

The case of inserting the copy prevention information into the PES header is useful in repeated transmission of information because copy prevention of a recording medium such as DSM is decided using CR and OC flags of the PES header and additional copy information field. In this case, there are a variety of copy prevention methods.

First, when a mode of "No Copy" is detected from the additional copy information field of the PES header, VCR B is not able to enter its recording mode.

Second, when a mode of "Copy Permitted" is detected in order to implement a copy prevention such as DAT mode, VCR B records but "No Copy" mode is recorded in the additional copy information field to interrupt recopying from a copying tape. This means that a secondary source tape may be formed from the original tape, the first source, but a third source tape cannot.

Third, for "Back-up Copy", tape B copied from VCR B is reproducible normally only in VCR A. According to

this method, reproducing-side VCR A encrypts the bit stream with its own inherent key and records it on tape so that only reproducing-side VCR A decrypts the MPEG bit stream recorded on the tape. For every VCR set, a unique key is provided, encrypted by VCR's key and recorded on tape B. However, the VCR set for recording tape B is VCR B and tape B is encrypted by VCR A's key so that VCR A's key needs to be transmitted to VCR B with GA bit stream.

Accordingly, when the key information of VCR A is transmitted as a header in advance prior to the bit stream in the "Back-up Copy", it is recorded at the front end of tape B, which satisfies the insertion position of the copy prevention information mentioned before.

Here, as shown in Fig. 2, the position of the additional copy information field is varied within the PES header according to whether presentation time stamp (PTS)/Decoding time stamp (DTS) and DSM trick mode field are present or not. This varied position must be compensated. Here, information transmitted through the additional copy information is a copy prevention method to be performed by recording-side VCR B.

In case of recording the bit stream shown in Fig. 11A in the method of "Back-up Copy", the format of the bit stream recorded on tape is determined as shown in Fig. 11B.

Here, a header area added to the front of the MPEG bit stream is formed with a tape header start code, that is, the header identifier code, and a key field for storing key information. In case of encrypting the MPEG bit stream in units of GOP, encryption blocks are classified by the packet start code prefix and stream ID of the PES header. The encryption block is a basic unit of encryption and can change whether encryption is performed in units of the encryption block, and encryption algorithm and key selection. Here, the encryption blocks must not be encrypted until the additional copy information field of the PES header. Encryption is performed until the end of the encryption block after the additional copy information field. The first 'transmission header' is not encrypted.

The operation of performing the "Back-up Copy" mode by adding the header will be described below.

First, in copying, when recording data of tape A is encrypted, reproducing-side VCR A decrypts it using the key information of the key field so as to make message m. Its key information is added to the header and transmitted in the format of Fig. 11C.

Recording-side VCR B records the key information transmitted from reproducing-side VCR A on the header of copying tape B and then records the encrypted bit stream. Here, when the key information is transmitted from reproducing side to recording side, for security, a public-key encryption may be employed to the system because the information may be exposed to a pirate.

Such public-key encryption system ensures the secret of data even though the public-key is exposed but cannot be processed in real-time due to a great amount of calculation. Therefore, this system is not improper when the MPEG bit stream is encrypted directly. The

"Back-up Copy" can be implemented when the MPEG bit stream is encrypted using a block-cipher algorithm or stream-cipher algorithm such as DES and a key used is encrypted in the public-key encryption.

In this case, every VCR u incorporates encryption algorithm E^{PK_U} corresponding to the public-key and decryption algorithm D^{PK_U} corresponding to the secret key. Encryption algorithm E^{PK_U} takes a power key of VCR u , and decryption algorithm D^{PK_U} an internal key of VCR u .

Here, the internal key may be opened to the public. Reproducing-side VCR A transmits the internal key on the key field of the header because another VCR encrypts using the internal key. Recording-side VCR B randomly selects a key Y used in the block-cipher algorithm such as DES and encrypts it with the public-key encryption system using an external key E^{PK_A} . The result is recorded on the key field of copying tape B.

Sequentially, the data is divided into encryption blocks and encrypted and recorded in the block-cipher algorithm using key Y . In this method, the bit stream of Fig. 11D is recorded on copying tape B.

When copying tape B is reproduced in reproducing-side VCR A, key Y can be restored by decryption $D^{PK_A}[E^{PK_A}(Y)]$ in which data is decrypted properly. In other VCRs, key Y cannot be found, which disables the decryption of the bit stream.

An embodiment of the present invention shown in Fig. 5 for performing such operation will be described below.

When playback starts for tape copying, reproducing portion 1 detects data recorded on tape as shown in Fig. 11A, and amplifies it by a predetermined level. As shown in Fig. 11B, key inserting portion 2 adds a header having a tape header start code and key field to the GA bit stream of reproducing portion 1 shown in Fig. 11A. Copy prevention information is loaded on the additional copy information field of the PES header to form a format shown in Fig. 11C. Here, decrypting portion 3 decrypts the bit stream formed in key inserting portion 2 and transmits it as parallel data to the recording-side VCR via an interface.

When the bit stream of Fig. 11C is transmitted to the recording-side VCR via the interface, key detecting/correcting portion 4 detects the key field added to the bit stream and corrects the key field if necessary.

Copy prevention information detecting portion 5 searches the PES header area to detect the additional copy information field. Here, though a slight amount of information is recorded in the additional copy information field, redundancy is provided in several areas of the bit stream to increase reliability of information transmitted.

Copy prevention information detecting portion 5 extracts the value of AC flag from the PES header flag in order to calculate the position of the additional copy information field because it varies within the PES header. Here, when copy prevention information correcting portion 6 corrects the output of copy prevention information detecting portion 5, encrypting portion 7 performs

encryption using the block-cipher algorithm such as DES. Here, copy prevention information correcting portion 6 performs correction while the input data is stored in a RAM. Accordingly, encrypting portion 7 records the encrypted bit stream on tape in recording portion 8. Because the key information of the reproducing-side VCR is added on the copying tape, only a VCR having this key information can reproduce tape normally.

As shown in Fig. 6, in copy prevention information detecting portion 5, PES header detecting portion 10 searches the output of key detecting/correcting portion 4 and outputs a header detection signal is-PES-header. After header detection signal is-PES-header is input, copy prevention information extractor 20 detects the additional copy information field and OC and CR flags.

PES header detector 10 for detecting the PES header is formed as shown in Fig. 7. When bit stream data_{in} is input as shown in Fig. 8A, first flipflop 11 synchronized to clock clk is delayed for a predetermined time to output the bit stream delayed as shown in Fig. 8B. Second flipflop 12 delays the output of first flipflop 11 by a predetermined time and outputs the bit stream delayed as shown in Fig. 8C.

Here, packet start code detecting portion 13 searches the bit stream shown in Fig. 8A and the output of first and second flipflops 11 and 12 shown in Figs. 8B and 8C in order to detect the packet start code of the PES header. When detection signal is-pscp is output as shown in Fig. 8D, delay 15 in which flipflops are coupled at multi-stages delays it sequentially according to clock clk .

Meanwhile, stream ID code detector 14 searches the output of second flipflop 12 and detects the stream ID area of the PES header. Then, detection signal is-sid shown in Fig. 8E is output to detection signal generator 16. Detection signal generator 16 logically multiplies the outputs of delay 15 and stream ID code detector 14, and the flipflops hold the output of the AND gate according to clock clk so that PES header detection signal is-PES-header is output to copy prevention information extractor 20 as shown in Fig. 8F.

Here, copy prevention information extractor 20 for detecting the copy prevention information is formed as shown in Fig. 9. When the parallel data output from PES header detector 10 and shown in Fig. 10A is held and output as shown in Fig. 10B, D-flipflop 22 synchronized to PES header detection signal is-PES-header of PES header detector 10 shown in Fig. 8F holds voltage +5V so that a HIGH signal is output to the clear ports of D-flipflops 23, 24 and 27 to release the clear states.

D-flipflop 23 is synchronized to the CR flag of the output of D-flipflop 21 shown in Fig. 10B to hold voltage V_{cc} so that a HIGH signal LCR is output as shown in Fig. 10C. D-flipflop 24 is synchronized to the OC flag of the output of D-flipflop 21 to hold voltage V_{cc} so that a HIGH signal LOC is output as shown in Fig. 10D.

Copy prevention position detector 25 searches the PD, TM and AC flags of the parallel data of PES header detector 10 shown in Fig. 10A to calculate the position

of the additional copy information field, which is output to counter 26 as shown in Fig. 10E. Counter 26 receiving the 4-bit value performs counting so that a HIGH signal is output as shown in Fig. 10F at a predetermined counting value.

D-flipflop 27 synchronized to HIGH output rco of counter 26 holds the additional copy information field from the parallel data of D-flipflop 21 shown in Fig. 10B. The field is output as shown in Fig. 10G.

As described above, in the copy prevention method and apparatus for a digital video system described, a key information is recorded with a bit stream so that a VCR having the key information reproduces tape normally, thereby preventing illegal copy of tape. In addition, for key information transmission, the public-key encryption is introduced to disable a pirate to release the copy prevention, increasing reliability of copy prevention.

Claims

1. A copy prevention method for a digital video system comprising the steps of:
 - (a) adding a header area of a header start code and key field to a reproduced bit stream;
 - (b) decrypting and transmitting the bit stream to which the header area is added;
 - (c) detecting a key field of the decrypted and transmitted bit stream and detecting copy prevention information; and
 - (d) encrypting the bit stream according to information detected from step (c) and recording it on tape.
2. A copy prevention method for a digital video system as claimed in claim 1, wherein, in step (a), a tape header start code is added to the front of the bit stream and then a key field thereto.
3. A copy prevention method for a digital video system as claimed in claim 1, wherein, in step (c), a key field is detected to be corrected if necessary.
4. A copy prevention method for a digital video system as claimed in claim 1, wherein, in step (c), copy prevention information is recorded in an additional copy information field of a PES header.
5. A copy prevention method for a digital video system as claimed in claim 4, wherein, in step (c), the variable position of said additional copy information field is compensated.
6. A copy prevention method for a digital video system as claimed in claim 1, wherein, in step (d), the bit stream is encrypted in units of block.
7. A copy prevention method for a digital video system as claimed in claim 1, wherein, in step (d), the bit stream is encrypted after an additional copy information field.
8. A copy prevention method for a digital video system as claimed in claim 6, wherein, in step (d), the bit stream is encrypted after the additional copy information field.
9. A copy prevention apparatus for a digital video system comprising:
 - a reproduction block for adding key information to a reproduced bit stream, and decrypting and transmitting it; and
 - a recording block for searching key information of the bit stream transmitted from said reproduction block to extract copy prevention information, and encrypting and recording the bit stream according to the extracted copy prevention information.
10. A copy prevention apparatus for a digital video system claimed in claim 9, wherein said reproduction block comprises:
 - reproduction means for reproducing data recorded on tape;
 - key insertion means for adding key information to the bit stream of said reproduction means; and
 - decryption means for decrypting the output of said key insertion means and transmitting it to a recording-side VCR.
11. A copy prevention apparatus for a digital video system as claimed in claim 9, wherein said recording block comprises:
 - key detecting/correcting means for detecting key information from the transmitted bit stream of a reproducing-side VCR;
 - copy prevention information detecting means for searching the key information detected from said key detecting/correcting means to detect copy prevention information;
 - encrypting means for encrypting the bit stream according the copy prevention information of said copy prevention information detecting means; and
 - recording means for recording the bit stream encrypted in said encrypting means.
12. A copy prevention apparatus for a digital video system as claimed in claim 11, wherein said copy prevention information detecting means comprises:
 - a PES header detecting portion for detecting a PES header from parallel data output from said key detecting/correcting means; and
 - a copy prevention information extractor enabled by a PES header detection signal of said PES header detecting portion to detect an additional copy information field.

13. A copy prevention apparatus for a digital video system as claimed in claim 12, wherein said PES header detecting portion comprises:
- first delay for delaying said parallel data output from said key detecting/correcting means in synchronization with a clock; 5
 - a packet start code detector for searching the input/output data of said first delay to detect a packet start code;
 - a second delay for delaying the output of said packet start code detector for a predetermined time; 10
 - a stream ID detector for searching the output of said second delay to detect a stream ID;
 - a detection signal generator for logically combining the outputs of said stream ID detector and said second delay and outputting a PES header detection signal. 15
14. A copy prevention apparatus for a digital video system as claimed in claim 13, wherein said first and second delays are constructed to connect D-flipflops at multi-stages serially. 20
15. A copy prevention apparatus for a digital video system as claimed in claim 13, wherein said detection signal generator comprises: 25
- an AND gate for logically multiplying the packet start code detection signal of said packet start code detector and the stream ID code detection signal of said stream ID code detector; and 30
 - a flipflop for holding the output of said AND gate in synchronization with a clock.
16. A copy prevention apparatus for a digital video system as claimed in claim 12, wherein said copy prevention information extractor comprises: 35
- a first flipflop for holding the parallel data output from said PES header detector;
 - a second flipflop enabled by a CR flag of the output of said first flipflop for holding a voltage; 40
 - a third flipflop enabled by a OC flag of the output of said first flipflop for holding a voltage;
 - a copy prevention information position operator for searching the parallel data output from said PES header detector and calculating the position of an additional copy information field; 45
 - a counter for counting the output of said copy prevention information position operator;
 - a fourth flipflop synchronized with the output of said counter for holding the additional copy information field of the output of said first flipflop; and 50
 - a fifth flipflop synchronized with a header detection signal of said PES header detector for enabling said second, third and fourth flipflops. 55

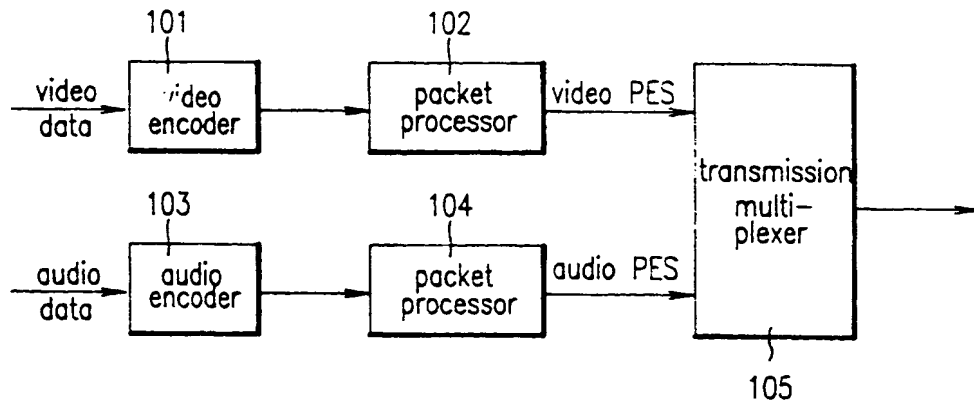
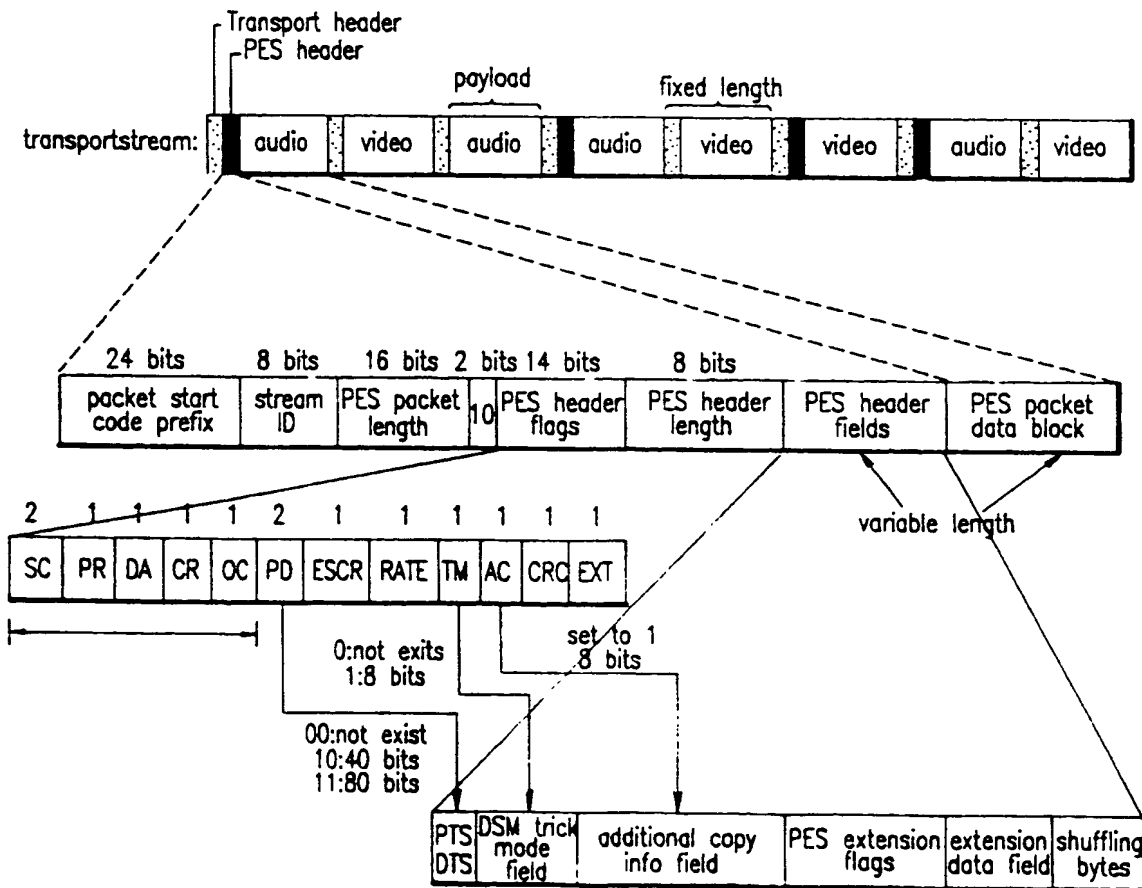
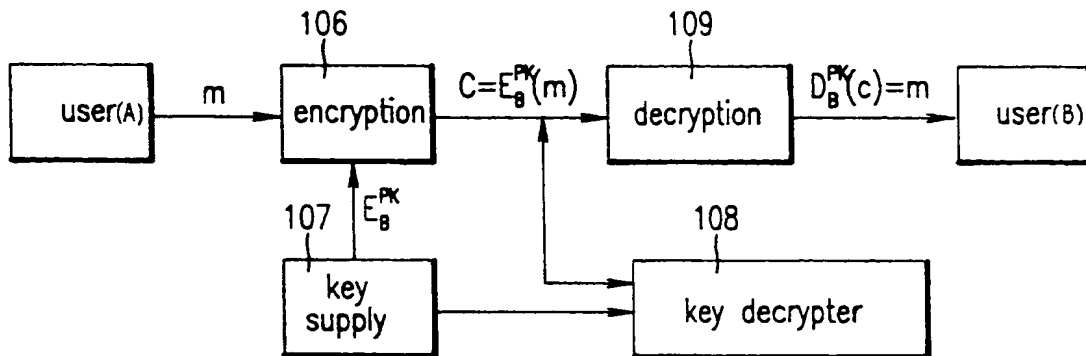
FIG. 1
prior art

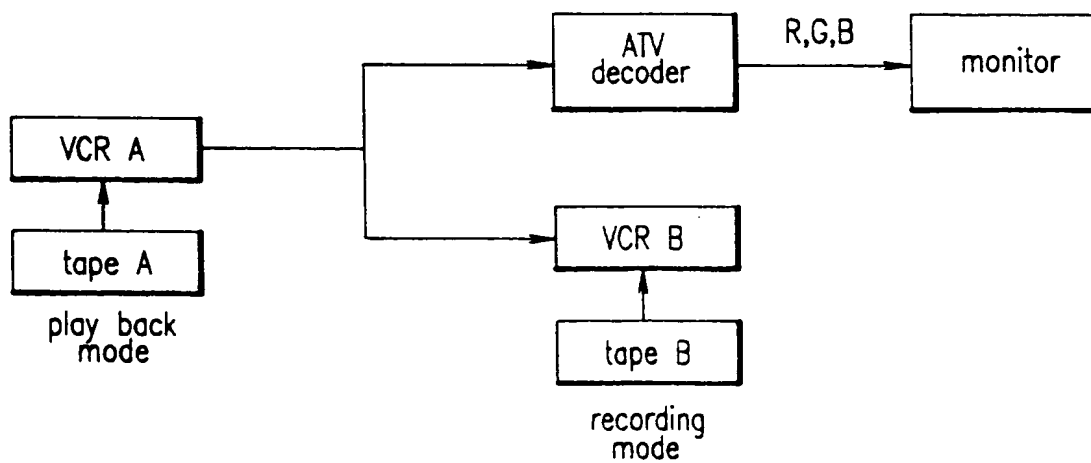
FIG. 2



F I G.3 prior art



F I G.4



F I G.5

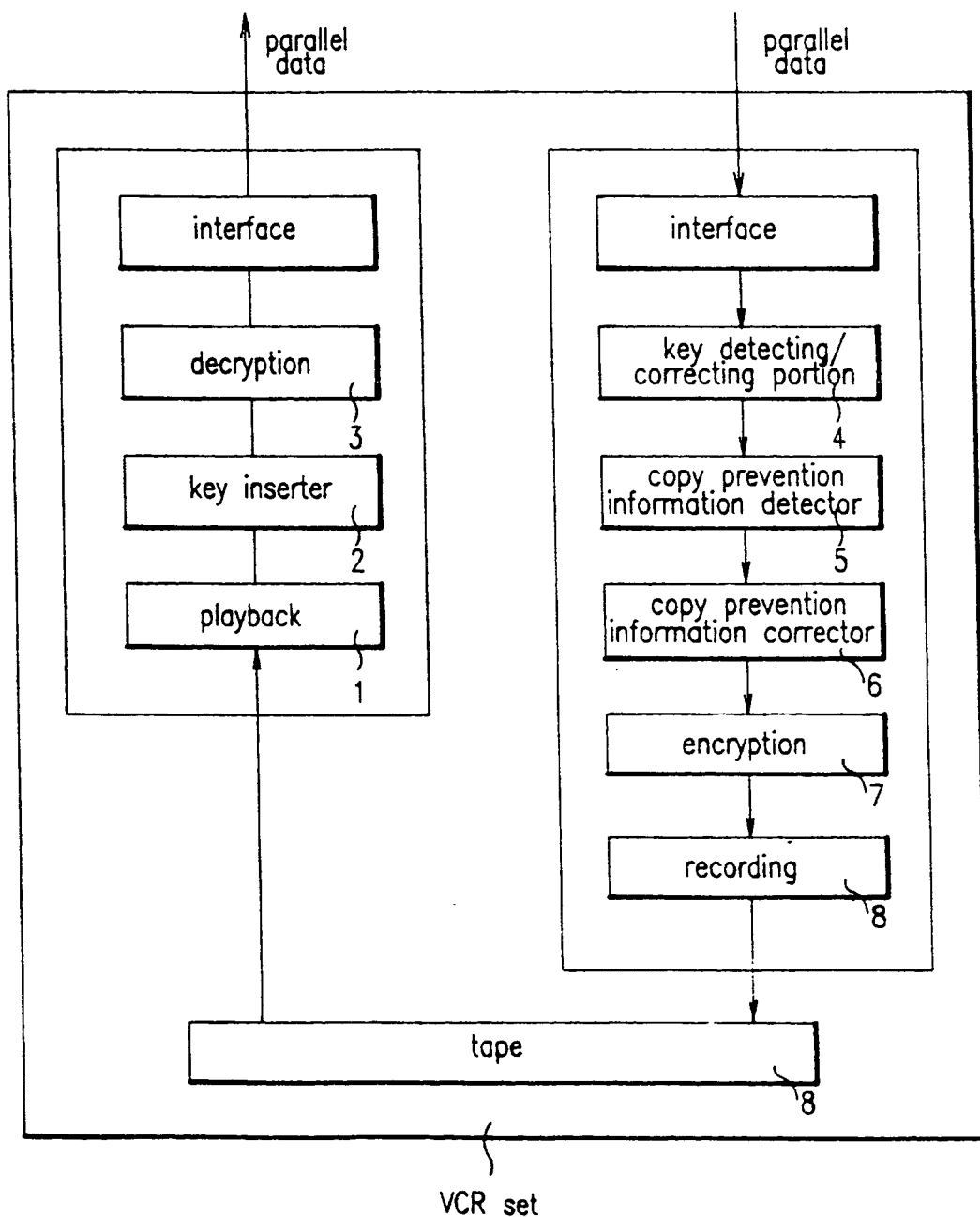


FIG. 6

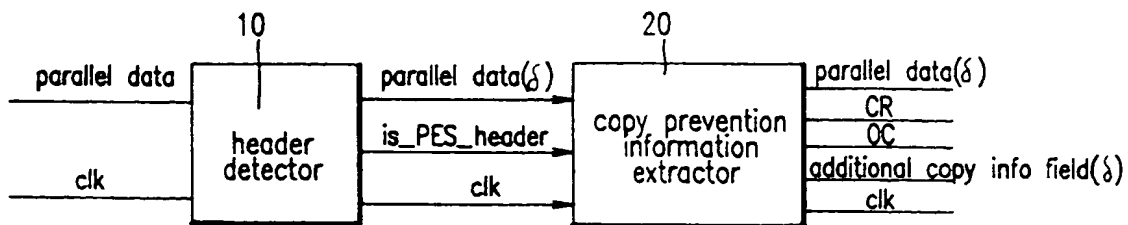
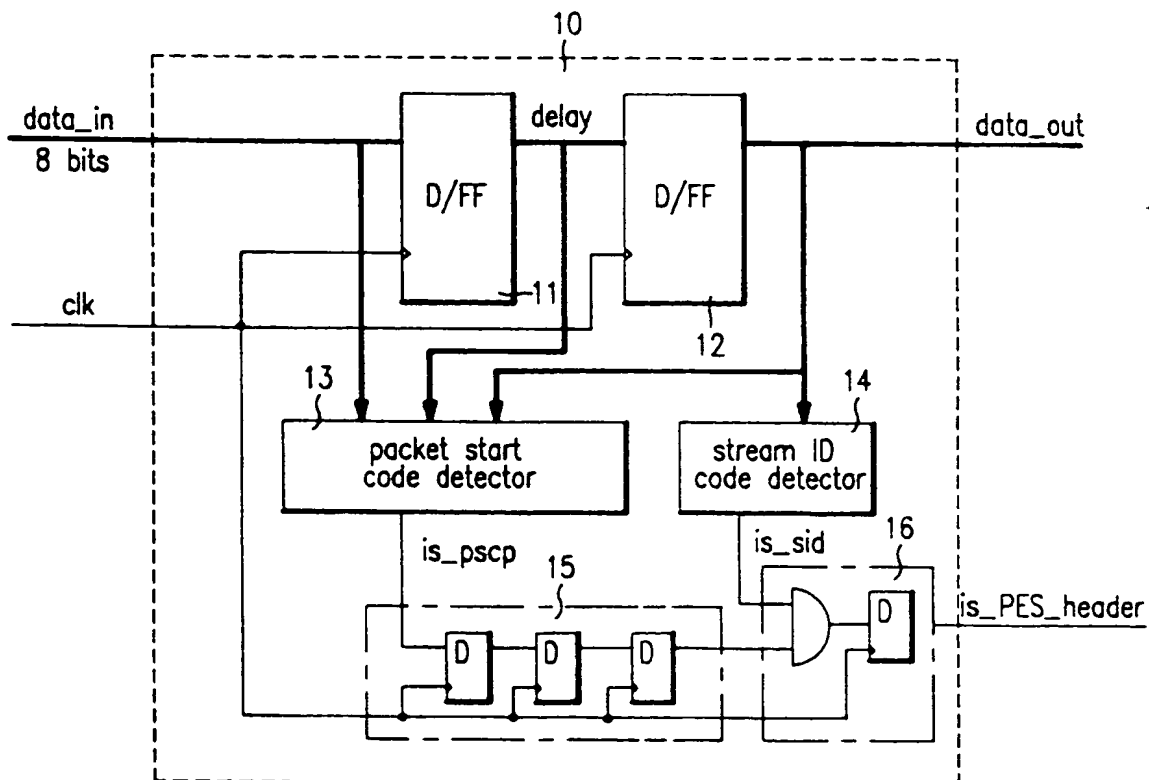
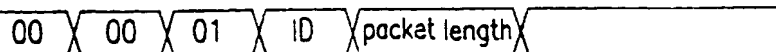


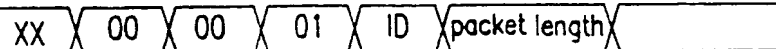
FIG. 7



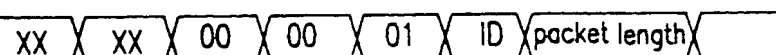
F | G.8a data_in



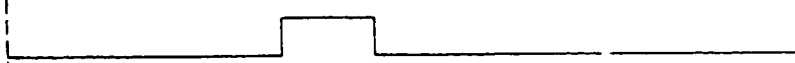
F | G.8b delay



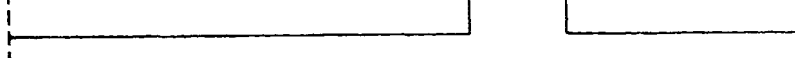
F | G.8c data_out



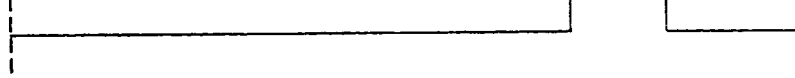
F | G.8d is_PSCP



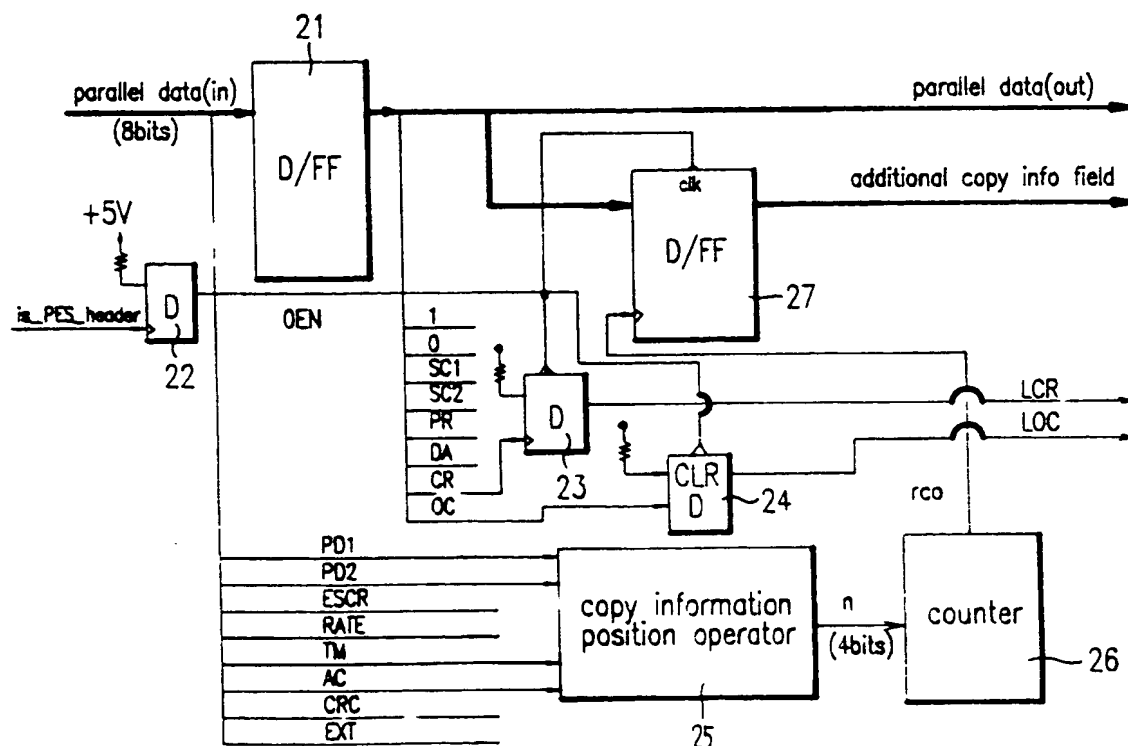
F | G.8e is_sid

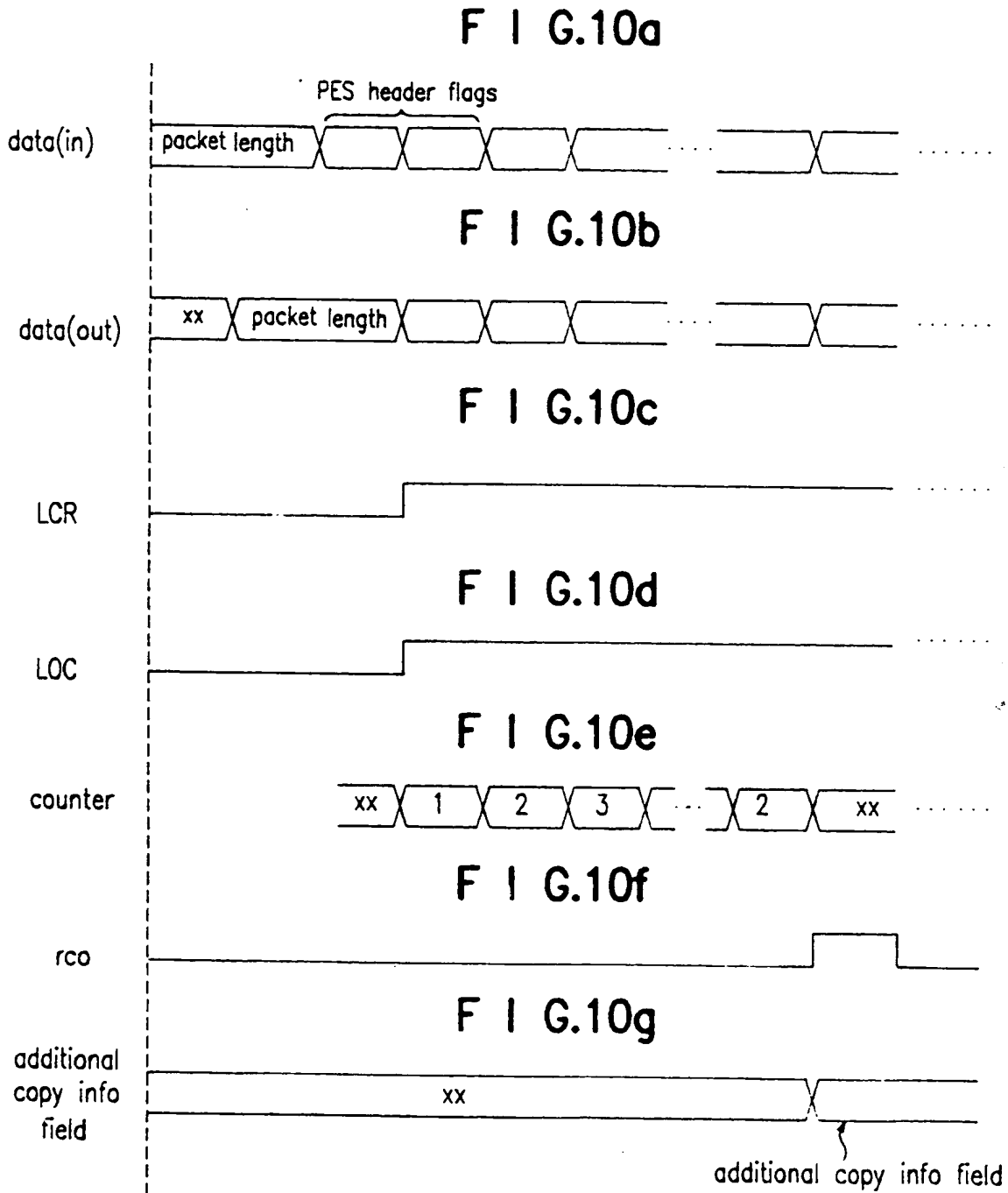


F | G.8f is_PES_header

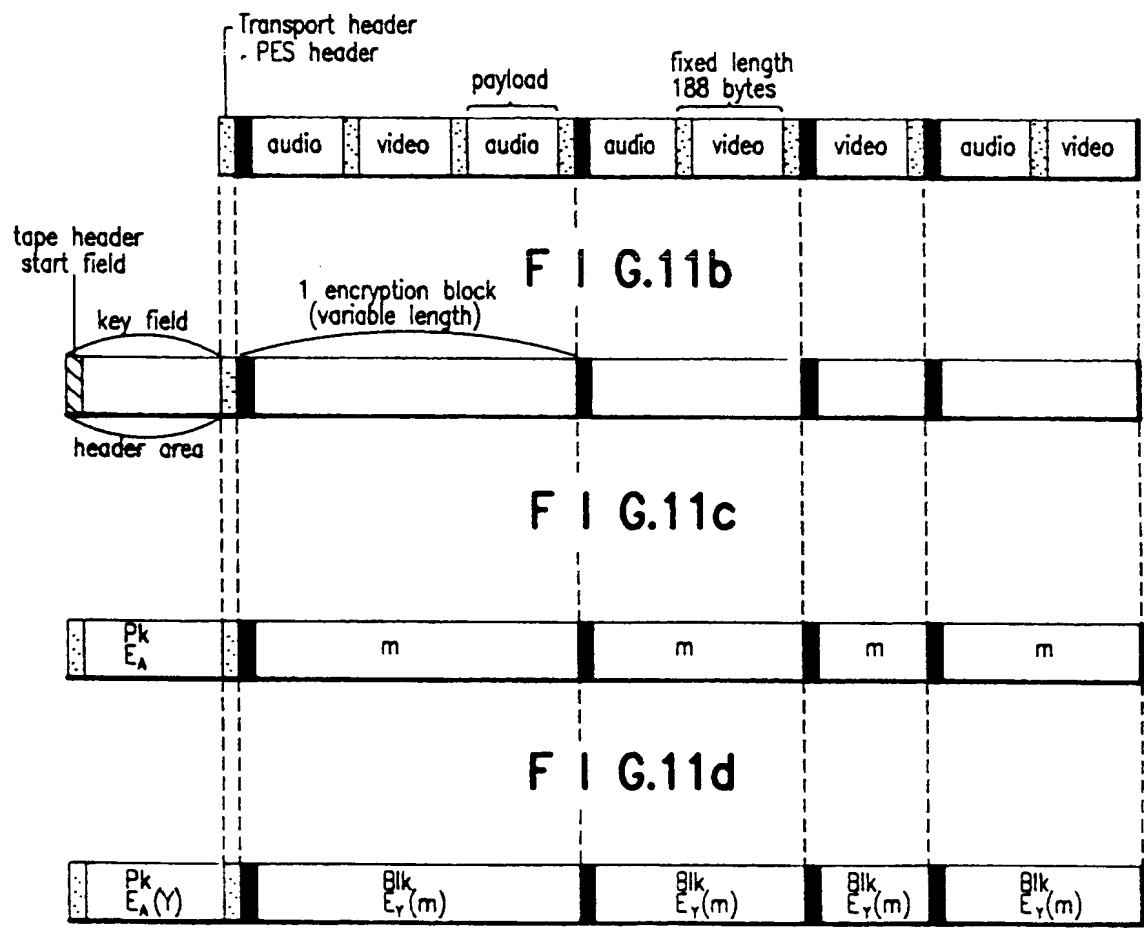


F | G.9





F I G.11a



(19)



Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 0 717 564 A3

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
16.07.1997 Bulletin 1997/29

(51) Int. Cl.⁶: **H04N 5/913**

(43) Date of publication A2:
19.06.1996 Bulletin 1996/25

(21) Application number: **95308482.9**

(22) Date of filing: **27.11.1995**

(84) Designated Contracting States:
DE FR GB

(30) Priority: **26.11.1994 KR 9431373**

(71) Applicant: **LG ELECTRONICS INC.**
Seoul (KR)

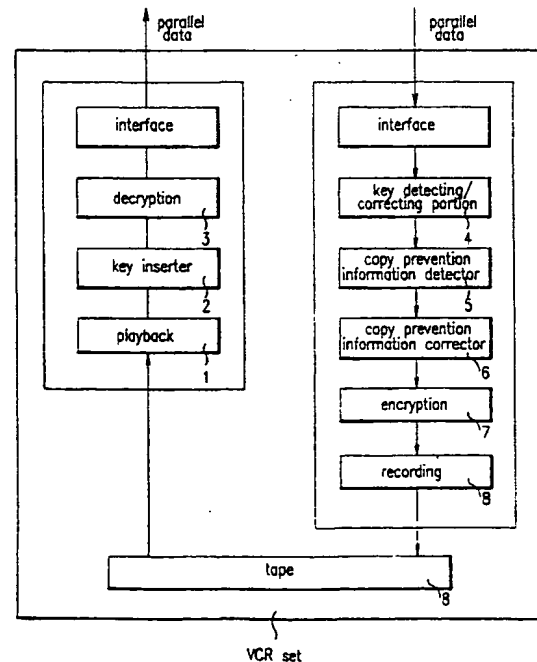
(72) Inventor: **Park, Tae Joon**
Seoul (KR)

(74) Representative: **Cross, Rupert Edward Blount et al**
BOULT WADE TENNANT,
27 Furnival Street
London EC4A 1PQ (GB)

(54) Copy prevention method and apparatus for digital video system

(57) A copy prevention method and apparatus for a digital video system is disclosed including the steps of: (a) adding a header area of a header start code and key field to a reproduced bit stream; (b) decrypting and transmitting the bit stream to which the header area is added; (c) detecting a key field of the decrypted and transmitted bit stream and detecting copy prevention information; and (d) encrypting the bit stream according to information detected from step (c) and recording it on tape.

F I G.5



EP 0 717 564 A3



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 95 30 8482

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	EP 0 589 459 A (MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.) * column 5, line 44 - column 7, line 43; figures 1-3 *	1,2,9-11	H04N5/913
A	EP 0 519 320 A (MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.) * column 5, line 56 - column 7, line 27; figures 2-4 *	1,9-11	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H04N
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 3 April 1997	Examiner Verleye, J
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03.92 (P04C01)